

Navid Shahrestani

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Education	B.S. in Electrical Engineering University of Florida, May 2012 GPA 3.93 M.S. in Electrical Engineering (currently ongoing) University of Florida, May 2013 GPA 4.0
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Experience	Accuplace Internship, Summer 2011 Worked on electrical system of a wind turbine generator that monitored status of turbine and helped in building and testing of turbine. Instigator, 2012 Competed in the ION Robotic Lawn Mower Competition in which an autonomous lawn mower platform was developed. Teaching Assistant, 2012 Digital Logic and Circuit's lab teaching assistant. IPPD Senior Design Project, 2011-2012 Lockheed Martin sponsored project designing and building an autonomous ATV platform. Biomedical Laboratory, 2011 Integrated several components and wrote assembly code for a microcontroller based system, which was used as a biomedical sensor to detect internal bleeding in patients. Intelligent Machine Design Lab Project, 2011 Created autonomous robot that avoided obstacles and crossed gaps that it encountered. Personal Project, 2011 Built digital power supply. Designed PCB and wrote code for menu system. Microfluidics Laboratory, 2008-2009 Manufactured chips used in protein detection using CNC mill and hydraulic press. Coded G-code to program mill and altered current designs to meet new specifications.
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Related Courses	Digital Design Microprocessor's	Electronic Circuits Linear Controls System	Machine Intelligence Design & Manufacturing
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Related Knowledge	C Assembly Eagle Quartus	VHDL Circuit Design SolidWorks Prolog/Lisp	FPGA/CPLD Altium Designer Various Microcontroller's PSPICE/LTSpice
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Honors & Special Qualifications	Electric E Award Merwin J. Larsen Award	Anderson Scholar Award Fluent in Spanish
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